

System-Level Design and Modeling: ESL Using C/C++, SystemC and TLM-2.0



This book discusses principles, methodologies and tools used for a modern hardware design process. Design flows and hardware languages needed for each stage of the design process are discussed, including C/C++ for description of gate and Boolean level logic circuits, SystemC for RTL descriptions, and TLM-2.0 for system level design and description. Following the coverage of languages and techniques for system level design, the book includes discussion of several system level architectures that can be described using C++, SystemC, and TLM-2.0.

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